Abstract of the Disclosur

An antenna detection and diagnostic system and associated method, including current and voltage detection circuits, are disclosed for detecting antenna failure mechanisms for integrated radio receivers. Integrated current limit detection circuitry is disclosed that determines current levels drawn by a remotely mounted antenna and generates a drive signal that controls a current limiting pass transistor such that current flow through the transistor is reduced when the detected current level drawn antenna rises above a current limit level. Integrated current and voltage detection circuitry is disclosed that detects current and voltage levels drawn by a remotely mounted antenna and determines under-current, over-current, and over-voltage antenna error conditions. At least one antenna error output pin for the integrated radio receiver is then used provide an output signal indicative of antenna error conditions.